



High Performance Computing



Vidyavardhini's College of Engineering & Technology (VCET)

NAAC and NBA Accredited (Autonomous Institute)

K. T. Marg, Vartak College Campus, Vasai Road (W),
Dist-Palghar, Vasai, Maharashtra 401202
Website- <https://www.vcet.edu.in>

NSM Sponsored Faculty Development Program on High Performance Computing

Organized by

Department of Computer Science and Data Science, Department of Information Technology, Department of Civil Engineering &

CDAC Pune

Under the aegis of National Supercomputing Mission, Government of India

In association with

Walchand College of Engineering, Sangli

(NSM Nodal Centre)

Dates: 1st March to 7th March 2026

(3 Days online, 3 Days offline)

Patron

Shri. Vikas Vartak, President, Vidyavardhini
Shri. Pandurang Naik, Vice President, Vidyavardhini
Shri. Hasmukhbhai Shah, Treasurer, Vidyavardhini
Mr. Ashish Kuvelkar, Senior consultant, CDAC, Pune
Shri Amol Chavan, Member Administrative Council, WCE, Sangli
Dr. S.S. Rathod, Director, WCE, Sangli
Dr. Rakesh Himte, Principal, VCET, Vasai

Convener

Dr. D. B. Kulkarni, Professor WCE, Sangli, (NSM Nodal Centre Head)

Coordinator

Dr. U. B. Chavan, WCE, Sangli
Dr. Satish S. Salunkhe, VCET, Vasai

Organizing Committee

Dr. Medha Shah, Nodal Center, Coordinator, WCE, Sangli
Ms. A. S. Pawar, Nodal Center Coordinator, WCE, Sangli
Dr. Thaksen Parvat, Professor, HOD, INFT, VCET, Vasai
Dr. Ajay Radke, Professor, HOD, Civil, VCET, Vasai

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Ms. Maya Varghese (CSEDS)
Ms. Puja Kadam (Civil)

Dr. Vikrant Agaskar (CSEDS)
Dr. V. Chandanshive (Civil)
Ms. Y. Kasture (Civil)



About Host Institute

Vidyavardhini's was established as a registered society in 1970 by late Padmashri H. G. alias Bhausaheb Vartak for the noble cause of education in rural areas. Vidyavardhini's College of Engineering and Technology, Vasai is located on the sprawling campus of Vidyavardhini's, spread over an area of 12.27 acres. It is a short, two minutes' walk from Vasai Road (W) Railway Station. The college is also accessible by road from Mumbai. Vidyavardhini's Society received approval from AICTE to start the new college of Engineering & Technology with effect from July 1994. The college is affiliated with the University of Mumbai. VCET offers UG programs in Computer Engineering, Computer Science and Engineering (Data Science), Information Technology, Artificial Intelligence and Data Science, Electronics and Telecommunication Engineering, Mechanical Engineering and Civil Engineering. The institute also offers a PG program in ME Structural Engineering (Civil), ME in Computer Engineering and MMS. The Institute is accredited by NAAC and has all eligible programs accredited by the National Board of Accreditation (NBA). Vidyavardhini's College of Engineering and Technology has been conferred Autonomous Status, marking a significant milestone in the institution's commitment to academic excellence and innovation.

About FDP

The programme will introduce several parallel programming models, including Open Multiprocessing (OpenMP) for shared memory machines, Message Passing Interface (MPI) for distributed memory machines, and Compute Unified Device Architecture (CUDA), a parallel computing platform developed by NVIDIA for exploiting GPU. It will also include hands-on practice on these programming models. It is primarily intended for professors who presently teach or plan to teach HPC-related courses. Researchers who use or plan to use parallel programming are also welcome to attend.

About Walchand College of Engineering

Walchand College of Engineering Sangli (WCE) was established in 1947 and aided by the Govt. of Maharashtra, is one of the oldest and premier engineering institutions in India with a beautiful campus of over 90 acres. WCE offers 6 UG programs in Civil, Electrical, Mechanical, Electronics, Information Technology and Computer Science & Engineering, 10 PG programs and Ph.D. programs. The institute has also been given autonomous status by UGC since 2007.

Course Objectives

- To introduce the fundamentals of parallel computing including parallel architectures, programming models and commonly used parallel programming constructs/libraries/languages
- To deliver hands-on sessions to provide experience applying HPC tools on both CPU and GPU platforms.
- To help faculties to include HPC courses in the curriculum.

Course Contents

- Understanding Multicore and Many core architectures
- Shared Memory Parallelism with OpenMP
- CUDA Programming
- Distributed Memory Parallelism with MPI
- Parallel Computing Patterns: Reduction, Matrix multiplication, convolution etc.
- Profiling sequential and parallel codes
- Performance measures in parallel computing

Target Audience

The workshop is open for all faculty members, research scholars working in all branches in faculty of Science and Technology. The registration is on a 'first come first serve' basis.

Important Dates

Registration Open: 24th February 2026

Last Date of Registration: 28th February 2026

Course Duration: 1st to 3rd March (Online) and 4th to 7th March (Offline) 2026

Mode of conduct: Hybrid (9:00 AM to 5:00 PM)

Registration Fees: 500/- (reimbursed after attending the FDP with Travelling Allowances Max. 2000 Rupees)

General Bank Name: Union Bank of India

Branch: Vasai Rd.

Account No. 320602011001031

IFSC Code: UBIN0562556

Registration Link:

<https://forms.gle/kywL82F29hb7SPE7>

Program benefits

- Interaction with eminent speakers from industry and academia
- Industry certifications worth ~21,000/- for FREE
- Access to HPC resources during the FDP
- Travelling Fare (max Rs. 2000), Food and Hotel accommodation will be provided on campus during offline sessions

Eminent Resource Persons

Mr. Ashish Kuvelkar, Senior Consultant, CDAC, Pune
Dr. D. B. Kulkarni, NSMNC Head, WCE, Sangli
Dr. U. B. Chavan, NSMNC Coordinator, WCE, Sangli
Dr. U. A. Deshpande, VNIT, Nagpur
Dr. S. R. Sathe, VNIT, Nagpur
Dr. R. K. Joshi, IIT Bombay
Dr. Sharad Sinha, IIT Goa
Dr. Nilesh Pikle, IIIT Nagpur
Dr. Sandip Shingade, V.J.T.I, Mumbai
Dr. Mandar Gurav, consultant HPC, Pune
Dr. Pramod Bide, SPIT, Mumbai

Contact Details

- Dr. Vikrant Agaskar: 9822836508
- Dr. Yogesh Pingle: 9422492389
- Mrs. Pooja Kadam: 9049987618

Email ID: yikrant.agaskar@vcet.edu.in
yogesh.pingle@vcet.edu.in
puja.ghadi@vcet.edu.in

Location: Scan Below code for Google Map

