University of Mumbai

EXAMINATION TIME TABLE

PROGRAMME - S.E. (VLSI Design & Technology) (Choice Based) (R-2022-23 'C' Scheme)

SEMESTER - IV

Days and Dates	Time	Paper Code	Paper
Tuesday, 13 May, 2025	02:30 p.m. to 05:30 p.m.	31141	Engineering Mathematics-IV
Thursday, 15 May, 2025	02:30 p.m. to 05:30 p.m.	31142	Electronic Devices & Circuits II
Monday, 19 May, 2025	02:30 p.m. to 05:30 p.m.	31143	Microcontrollers Applications
Wednesday, 21 May, 2025	02:30 p.m. to 05:30 p.m.	31144	Principles of Communication Engineering
Friday, 23 May, 2025	02:30 p.m. to 05:30 p.m.	31145	Signals & Systems

Important Note: •The candidates appearing for the examination should report 20 minutes before the start of examination.
   • Mobile phones and other elctronic gazets are prohibited in the examination hall.
   • Change if any, in the time table shall be communicated on the university web site.

Mumbai - 400 098
28th April, 2025.

Dr. Pooja Raundale
Director
Board of Examinations & Evaluation