

Founder President Late Padmashri H. G. Vartak

(Approved by AICTE and Affiliated to the University of Mumbai) NBA and NAAC Accredited

K. T. Marg, Vasai Road (W), Dist. Palghar - 401 202, Maharashtra. Tel.: 0250-2338234 (6 Line) • Email : vcet_inbox@vcet.edu.in • Website : www.vcet.edu.in

Date: 10/10/2024

DATE EXTENDED NOTICE FOR REVISE INSTITUTE LEVEL ADMISSION 2024-2025

As per notice dated 04th October 2024 for Revised Institute Level Admission 2024-25. Due to requests received from parents and students, the last date of fill & submission of application form (in an offline mode) for seats to be filled at <u>Institute Level</u> 2024-2025 has been extended upto 14th October 2024 upto 3.00 p.m. and Merit list will be displayed on 14th October 2024 at 4.00 p.m.

Consequently, the Further Schedule for Admission for seats to be filled at Institute Level 2024-2025 will be declared accordingly.

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DATE:

VACANCY AFTER CAP ROUNDS 2024-2025

As per revised notification received from CET CELL vide No. तंशिप्र-१२२४क्रप्र.Ë.०३क्र पवेशाचा अंतिम सुधारीत दिनांकक्र२०२४क्र१७२३ dated 01/10/2024, Applications are invited from the eligible candidates for admission to following courses for seats to be filled at Institute level against following CAP vacancies.

| SR.NO | BRANCH | VACANT SETAS |
|-------|--|--------------|
| 1 | F.E. CIVIL ENGINEERING | 14 |
| 2 | F.E. COMPUTER ENGINEERING | 02 |
| 3 | F.E. ELECTRONICS & TELE COMMUNICATION ENGINEERING | 01 |
| 4 | F.E. ELECTRONICS ENGINEERING (VLSI DESIGN & TECHNOLOGY) | 12 |
| 5 | F.E. MECHANICAL ENGINEERING | 13 |
| 6 | DSE ELECTRONICS ENGINEERING (VLSI DESIGN & TECHNOLOGY) | 40 |
| 7 | DSE COMPUTER SCIENCE & ENGINEERING (DATA SCIENCE) | 01 |
| 8 | M.E. CIVIL ENGINEERING (STRUCTURAL) | 02 |
| 9 | MASTER OF MANAGEMENT STUDIES | 74 |
| 10 | DSE IN MASTER OF MANAGEMENT STUDIES (LATERAL ENTERY) | 81 |

Desirous eligible candidates should fill and submit the application form available In Administration office before 10/10/2024 upto 4.00 pm.

Consequently, the Further Schedule for Admission for seats to be filled at Institute Level for First year Engg. 2024-25 will be declared accordingly.



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DATE: 09/09/2024

INSTITUTE LEVEL ADMISSION

FOR FIRST YEAR ENGINEERING – 2024-2025

CUT OFF MHT-CET 2024 PERCENTILE SCORE AFTER INSTITUTE LEVEL ROUND I

| BRANCH | VCET MERIT NO. | CUT OFF MERIT SCORE |
|---|-------------------|---------------------------|
| ARTIFICIAL INTELLIGENCE & DATA SCIENCE [AI & DS] | 23 | 95.9986676 |
| COMPUTER ENGINEERING [COMP] | 91 | 92.0214155 |
| INFORMATION TECHNOLOGY [IT] | 99 | 91.2379211 |
| COMPUTER SCIENCE & ENGINEERING (DATA SCIENCE) [CSE] | 115 | 89.6994627 |
| ELECTRONICS & TELE COMMUNICATION ENGINEERING [ENTC] | 144 | 87.920678 |
| ELECTRONICS ENGINEERING (VLSI DESIGN & TECHNOLOGY) | 232 | 81.3894523 |
| MECHANICAL ENGINEERING [MECH] | 260 | 79.5235744 |
| CIVIL ENGINEERING [CIVIL] | 302 | 76.033773 |

SD/-

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INSTITUTE LEVEL ADMISSION 2024-2025 SCHEDULE NOTICE Notice Date: 08/09/2024 Notice Expire Date: 15/09/2024

| 1 | Online Submission of Application form on given link on College Web site www.vcet.edu.in | 06/09/2024 UPTO 3.00 p.m. |
|---|---|--|
| 2 | Display of Provisional Merit List on College Web site www.vcet.edu.in | 09/09/2024 at 10.30 a.m. |
| 3 | Submission and redressal of Grievance (if any) in writing mail regarding the Merit List on admission@vcet.edu.in | 09/09/2024 up to 2.00 p.m. |
| 4 | Display of Final Merit List on College Web site www.vcet.edu.in | 09/09/2024 at 3.30 p.m. |
| 5 | Display of Allotment on College Web site www.vcet.edu.in | 09/09/2024 at 6.00 p.m. |
| 6 | Accepting the Offer as per Allotment and Confirm Admission by Reporting to the Institute Office # | 10/09/2024 to 11/09/2024 Time: 10.00 a.m. to 4.00 p.m. |
| 7 | Continue Rounds will be conducted as per availability of Vacancies | 12/09/2024 and 13/09/2024 Time: 10.00 a.m. to 4.00 p.m. |

The schedule displayed above is provisional and may change under unavoidable circumstances.

If any query regarding schedule, please contact on 0250-2334612

It is mandatory for the aspiring candidate to be present at the time of rounds listed above and candidate should bring all the original documents (for verification purpose) along with Demand Draft / Pay Order of `1,29,102/- without which admission will not be secured.

Candidates who have been allotted seat in scheduled Rounds and he doesn't secure his admission by paying college admission fees in the specified period, such candidates will not be eligible for allotted preferences in subsequent Rounds.

DEMAND DRAFT / PAY ORDER SHOULD BE IN FAVOUR OF "<u>VIDYAVARDHINI'S COLLEGE OF ENGINEERING &</u> <u>TECHNOLOGY</u> " AND PAYABLE AT MUMBAI



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ADMISSION SCHEDULE FOR SEATS TO BE FILLED AGAINST F.E. CAP VACANCIES (ACAP) 2024-2025 NOTICE

Notice Date: 08/09/2024 Notice Expire Date: 15/09/2024

| 1 | Online Submission of Application form on given link on College Web site www.vcet.edu.in | 06/09/2024 UPTO 3.00 P.M. |
|---|--|--|
| 2 | Display of Provisional Merit List on College Web site <u>www.vcet.edu.in</u> | 09/09/2024 at 10.30 a.m. |
| 3 | Submission and redressal of Grievance (if any) in writing mail regarding the Merit List on admission@vcet.edu.in | 09/09/2024 up to 2.00 p.m. |
| 4 | Display of Final Merit List | 09/09/2024 at 3.30 a.m. |
| 5 | Round of Admission 1 | CET Percentile Marks above 95.0000000 Time : 10/09/2024 at 9.00 a.m. |
| 6 | Display Vacancy After First Round (If Any) on College Web site www.vcet.edu.in | Time : 10/09/2024 at 12.30 p.m. |
| 7 | Accepting the Offer as per Allotment and Confirm Admission by Reporting to the Institute Office # | CET Percentile Marks above 90.0000000 to 95.0000000 Time : 10/09/2024 at 1.00 p.m. |
| 8 | Display Vacancy After Second Round (If Any) on College Web site <u>www.vcet.edu.in</u> | Time : 10/09/2024 at 5.30 p.m. |
| 9 | Accepting the Offer as per Allotment Round Second and Confirm Admission # | CET Percentile Marks below 90.0000000 Time : 11/09/2024 at 9.00 p.m. |

The schedule displayed above is provisional and may change under unavoidable circumstances.

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It is mandatory for the aspiring candidate to be present at the time of rounds listed above and candidate should bring all the original documents (for verification purpose) along with Demand Draft / Pay Order of `1,29,102/- without which admission will not be secured.

Candidates who have been allotted seat in scheduled Rounds and he doesn't secure his admission by paying college admission fees in the specified period, such candidates will not be eligible for allotted preferences in subsequent Rounds.

DEMAND DRAFT / PAY ORDER SHOULD BE IN FAVOUR OF "<u>VIDYAVARDHINI'S COLLEGE OF ENGINEERING &</u> <u>TECHNOLOGY</u> " AND PAYABLE AT MUMBAI

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Date : 24/08/2024

NOTICE NO. 35

DATE EXTENDED NOTICE FOR FIRST YEAR ENGINEERING INSTITUTE LEVEL ADMISSION 2024-2025

Due to requests received from parents and students, the last date of fill & submission of application form (Online / Offline) for seats to be filled at <u>Institute</u> <u>Level</u> for First year Engg. 2024-2025 has been extended upto 06th September 2024 upto 3.00 p.m.

Consequently, the Further Schedule for Admission for seats to be filled at Institute Level for First year Engg. 2024-2025 will be declared accordingly.

Sd/-PRINCIPAL



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