

Program: Information Technology

Curriculum Scheme: Revised 2012

Examination: Third Year Semester V

Course Code: **TEITC503** and Course Name: **MICROCONTROLLER AND EMBEDDED SYSTEMS**

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Note to the students:- All the Questions are compulsory and carry equal marks .

Q1.	Which one of the following is the successor of 8086 and 8088 processor?
Option A:	80286
Option B:	80387
Option C:	8051
Option D:	8087
Q2.	8051 series has how many 16 bit registers?
Option A:	2
Option B:	3
Option C:	1
Option D:	0
Q3.	DAA command adds 6 to the nibble if:
Option A:	CY and AC are necessarily 1
Option B:	either CY or AC is 1
Option C:	no relation with CY or AC
Option D:	CY is 1
Q4.	ARM processors where basically designed for ____
Option A:	Main frame systems
Option B:	Distributed systems
Option C:	Mobile systems
Option D:	Super computers
Q5.	Which of the following provides a buffer between the user and the low-level interfaces to the hardware?
Option A:	operating system
Option B:	kernel
Option C:	software
Option D:	hardware
Q6.	Which are the processors based on RISC?
Option A:	SPARC
Option B:	80386
Option C:	MC68030
Option D:	MC68020

Q7.	How many bytes of bit addressable memory is present in 8051 based microcontrollers?
Option A:	8 BYTES
Option B:	32 BYTES
Option C:	16 BYTES
Option D:	128 BYTES
Q8.	In 8 bit signed number operations, OV flag is set to 1 if:
Option A:	a carry is generated from D7 bit
Option B:	a carry is generated from D3 bit
Option C:	a carry is generated from D7 or D3 bit
Option D:	a carry is generated from D7 or D6 bit
Q9.	The additional duplicate register used in ARM machines are called as _____
Option A:	Copied-registers
Option B:	Banked registers
Option C:	Extra registers
Option D:	Extential registers
Q10.	Which of the following are not dependent on the actual hardware performing the physical task?
Option A:	applications
Option B:	registers
Option C:	hardware
Option D:	parameter blocks
Q11.	Name the processor which helps in floating point calculations.
Option A:	microprocessor
Option B:	microcontroller
Option C:	coprocessor
Option D:	controller
Q12.	How are the bits of the register PSW affected if we select Bank2 of 8051?
Option A:	PSW.5=0 and PSW.4=1
Option B:	PSW.2=0 and PSW.3=1
Option C:	PSW.3=1 and PSW.4=1
Option D:	PSW.3=0 and PSW.4=1
Q13.	Which instructions have no effect on the flags of PSW?
Option A:	ANL
Option B:	ORL
Option C:	XRL
Option D:	All of the mentioned
Q14.	The address system supported by ARM systems is/are _____

Option A:	Little Endian
Option B:	Big Endian
Option C:	X-Little Endian
Option D:	Both Little & Big Endian
Q15.	Which forms the heart of the operating system?
Option A:	kernel
Option B:	applications
Option C:	hardware
Option D:	operating systems