

Program: BE Electronics & Telecommunication Engineering

Curriculum Scheme: Revised 2016

Examination: Third Year Semester V

Course Code: ECC501 and Course Name: Microprocessor & Peripherals Interfacing

Time: 1 hour

Max. Marks: 50

=====

Note to the students:- All the Questions are compulsory and carry equal marks .

Q1.	In 8086, the BIU contains FIFO register of size _____ bytes.
Option A:	8
Option B:	6
Option C:	4
Option D:	12
Q2.	Programmable timer is used to generate timing signal: _____ and the counter starts counting only if _____.
Option A:	8251, CLK signal is low
Option B:	8259, CLK signal is high
Option C:	8254, GATE signal is high
Option D:	8255, GATE signal is low
Q3.	The effect of the following instructions mov ah, 02h int 21h
Option A:	Read a character into al
Option B:	Read a character into dl
Option C:	Display character in al
Option D:	Display character in dl
Q4.	_____ input is available, so that another coprocessor can be connected and function in _____ with the 8087.
Option A:	RQ/GT0, parallel
Option B:	RQ/GT1, parallel
Option C:	QS1 & QS0, parallel
Option D:	S0 & S1, parallel
Q5.	_____ is a program that converts Assembly language to Machine Language and its example is _____.
Option A:	Assembler, TASM
Option B:	Assembler, Turbo C
Option C:	Compiler, Turbo C

Option D:	Compiler, Borland C
Q6.	When non-specific EOI command is issued to 8259A it will automatically _____ .
Option A:	set the ISR
Option B:	reset the ISR
Option C:	set the INTR
Option D:	reset the INTR
Q7.	In 8086 microprocessor one of the following instructions is executed before an arithmetic operation.
Option A:	AAM
Option B:	AAD
Option C:	DAS
Option D:	DAA
Q8.	If 64K ROM (2 chips of 32K each) are interfaced with 8086. Assuming that last physical address of ROM is FFFFFH, what will be starting and ending address of each chip?
Option A:	ROM 1: F8000H-FFFFEH, ROM 2: F8001H-FFFFFH
Option B:	ROM 1: F8001H-FFFFFH, ROM 2: F8000H-FFFFEH
Option C:	ROM 1: F0000H-FFFFEH, ROM 2: F0001H-FFFFFH
Option D:	ROM 1: F0001H-FFFFFH, ROM 2: F0000H-FFFFEH
Q9.	An n bit ADC using V as reference has a resolution of ____ .
Option A:	$\frac{1}{2^n}$
Option B:	$\frac{V}{2^n - 1}$
Option C:	$V(n)$
Option D:	2 V (n)
Q10.	What is the output of the following code AL= -28 decimal, BL=59 decimal IMUL BL; AX=?, MSB=?
Option A:	AX= F98CH, MSB=1
Option B:	AX= 1652 decimal, MSB=1
Option C:	BX F9C8H, MSB=1
Option D:	BX= 1652 decimal, MSB=1
Q11.	The primary function of the _____ is to accept data from I/P devices.
Option A:	multiprocessor
Option B:	microprocessor
Option C:	peripherals
Option D:	interfaces

Q12.	The DMA request input pin that has the highest priority is _____ .
Option A:	DREQ ₀
Option B:	DREQ ₁
Option C:	DREQ ₂
Option D:	DREQ ₃
Q13.	The no. of address lines required to address a memory of size 32 K is _____
Option A:	14 lines
Option B:	15 lines
Option C:	16 lines
Option D:	18 lines
Q14.	Digital acquisition systems are used when _____ .
Option A:	bandwidth is high
Option B:	bandwidth is medium
Option C:	bandwidth is low
Option D:	bandwidth is zero
Q15.	One of the following is wrong combination of registers
Option A:	ES : DI
Option B:	SS : BP
Option C:	DS : SS
Option D:	CS : IP
Q16.	For 8086, what is the output of the following code PUSH AL
Option A:	Decrement SP by 2 & push a word to stack
Option B:	Increment SP by 2 & push a word to stack
Option C:	Decrement SP by 2 & push a AL to stack
Option D:	Illegal
Q17.	The technique of assigning a memory address to each I/O device in the computer system is called: _____ .
Option A:	memory-mapped I/O
Option B:	ported I/O
Option C:	dedicated I/O
Option D:	wired I/O
Q18.	_____ instruction can be used to complement any bit of destination.
Option A:	NOT
Option B:	OR
Option C:	XOR
Option D:	AND
Q19.	Digital to Analog Conversion is _____ Analog to Digital Conversion.
Option A:	less complex than

Option B:	more complex than
Option C:	as complex as
Option D:	unpredictable
Q20.	_____ interrupt occurs when the result of a division is too large to be stored and its ISR address is stored at location _____ in the interrupt vector table.
Option A:	Overflow, 00010H
Option B:	NMI, 00008H
Option C:	INT0, 00000H
Option D:	INT3, 0000CH
Q21.	Which of the following instructions are used for searching a byte in a string of bytes?
Option A:	MOVSB
Option B:	CMPSB
Option C:	SCASB
Option D:	SCASW
Q22.	Every memory location contains _____ of data.
Option A:	4 bits
Option B:	8 bits
Option C:	16 bits
Option D:	32 bits
Q23.	If analog voltage of 3.2V is connected to the IN3 channel of ADC 0809. Comment on the digital equivalent expected if VREF is 5V.
Option A:	A3 H
Option B:	FF H
Option C:	AB H
Option D:	32 H
Q24.	In the I/O mode, the 8255 ports work as _____ .
Option A:	reset pins
Option B:	set pins
Option C:	programmable I/O ports
Option D:	only output ports
Q25.	8086 has _____ address bus and _____ data bus.
Option A:	20-bit, 16-bit
Option B:	16-bit, 8-bit
Option C:	16-bit, 20-bit
Option D:	32-bit, 16-bit