

## Vidyavardhini's College of Engineering & Technology Department of Electronics and Telecommunication Engineering

3.5.1 2019-

## **MoU** Activities

Organization with which MoU is signed	Date of MoU signed	From	То	Purpose and Activities	Number of students/teachers who participated under MoUs
Logiq Embedded Systems India Pvt Ltd	16/07/2019- 16/07/2022	02/12/2019	25/12/2019	Internship	1 Student
		01/07/2019	01/06/2020	Project Guidance	3 Students

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